Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"6141196".PN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/12 13:07
S2	54	(INDUCTION adj motor) and ((negative and positive) adj sequence)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/12 13:07
S3	122	(motor) and ((negative and positive) adj sequence)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/12 13:08
S4	52	(motor) and ((negative and positive) adj sequence) and phasor	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/12 13:08
S5	39	(motor) and ((negative and positive) adj sequence) and phasor and (computer or processor or micro-processor or microprocessor or micro adj processor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/12 13:09
S6	0	("5345158" or "6042265").pn. and "20020053894"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/12 13:37
S7	2	("5345158" or "6042265").pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/12 13:37
S8	1	"20020053894"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/04/12 13:39
S9	125	motor and (positive adj sequence and negative adj sequence)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/10/17 14:58
S10	91	motor and (positive adj sequence and negative adj sequence) and (controller or microcontroller or micro-controller or micro adj controller or processor or microprocessor or micro-processor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/10/17 14:59
S11	37	motor and (positive adj sequence and negative adj sequence) and (controller or microcontroller or micro-controller or micro adj controller or processor or microprocessor or micro-processor) and phasor$2	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/10/17 14:59

| S12 | 12 | motor and (positive adj sequence and negative adj sequence) and (controller or microcontroller or micro-controller or micro adj controller or processor or microprocessor or micro-processor) and phasor$2 and (efficienc$4 or optimum or power adj factor) | US-PGPUB; USPAT; EPO; JPO | OR | ON | 2005/10/17 15:00 |